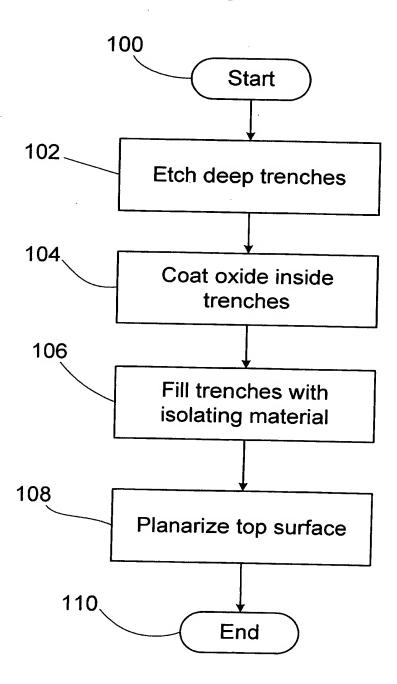
INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL ISOLATION AND METHOD FOR MAKING by James William KRETCHMER, et al. U.S. Patent Application No.: To Be Assigned Attorney Docket No.: 134765/H&W Docket No.: 61765.005020 Filed November 6, 2003

Figure 1



INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL ISOLATION AND METHOD FOR MAKING by James William KRETCHMER, et al.
U.S. Patent Application No.: To Be Assigned Attorney D cket No.: 134765/H&W Docket No.: 61765.005020 Filed November 6, 2003



Figure 2

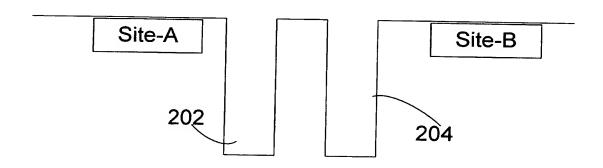


Figure 3

INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL ISOLATION AND METHOD FOR MAKING by James William KRETCHMER, et al.
U.S. Patent Application No.: To Be Assigned Attorney Docket No.: 134765/H&W Docket No.: 61765.005020 Filed November 6, 2003

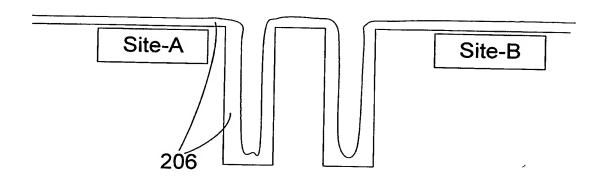


Figure 4

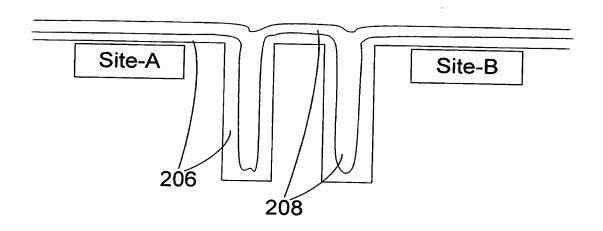


Figure 5

INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL ISOLATION AND METHOD FOR MAKING by James William KRETCHMER, et al.
U.S. Patent Application No.: To Be Assigned Attorney Docket No.: 134765/H&W Docket No.: 61765.005020 Filed November 6, 2003

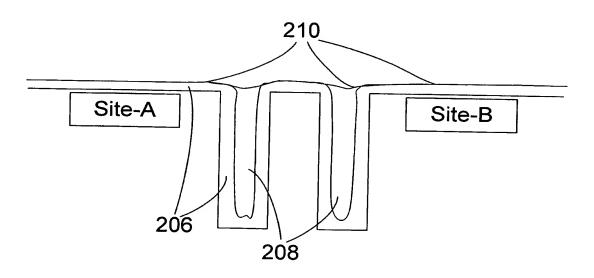


Figure 6

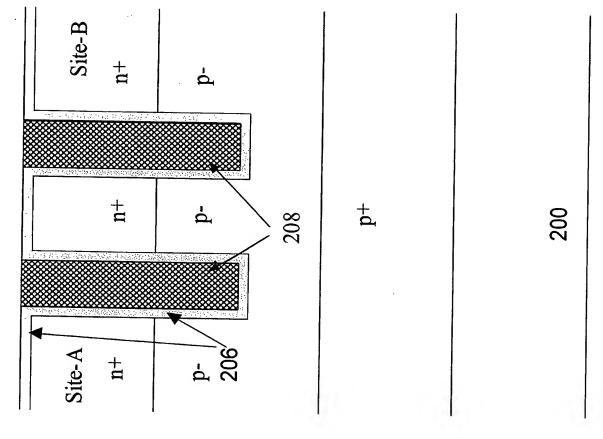
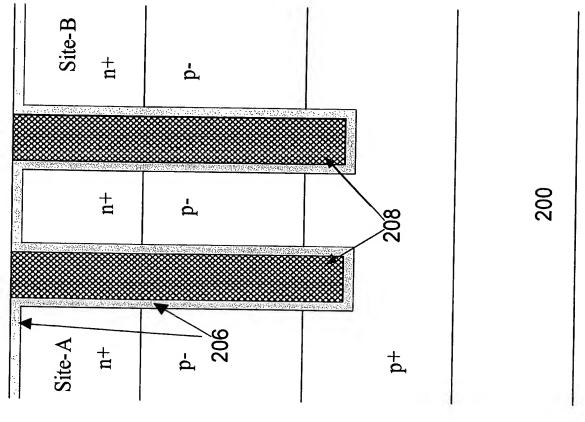
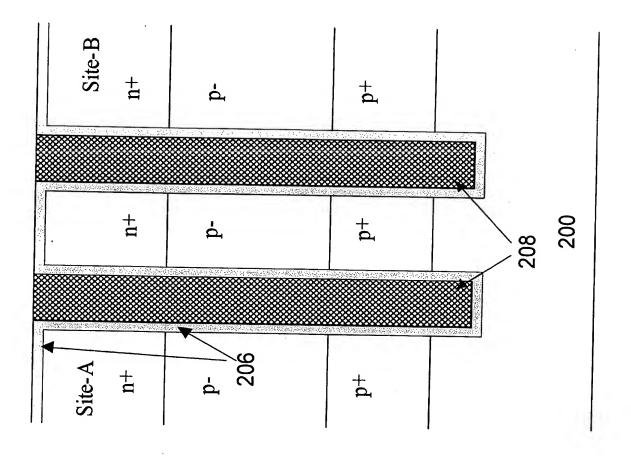


Figure 8





INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL INTEGRATED DEVICES WITH OPTICAL AND ELECTRICAL DAY James William KRETCHMER, et al. U.S. Patent Application No.: To Be Assigned Attorney Docket No.: 134765/H&W Docket No.: 61765.005020 Filed November 6, 2003

210 Figure 10 \mathbf{p}^{+} ď 늄 td ď 200 Site-A $^{+}$ u $^{+d}$

equiv. circuit

